

SEARCHED	INDEXED	SERIALIZED	FILED	CLASSIFICATION	PATENT NUMBER
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U.S. UTILITY Patent Application					
O.I.P.E.			PATENT DATE		
12/12/02			01/02/03		
APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/943843	F	438	240	2812 JRB	FOURSON W. ELI
APPLICANTS	Tomio Iwasaki Hideo Miura Hiroyuki Ohta Hiroshi Moriya				
TITLE	Semiconductor device and method of manufacturing the same				

ISSUING CLASSIFICATION		CROSS REFERENCE(S)				
CLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
1	2	3	4	5	6	7
8	9	10	11	12	13	14
ISSUING CLASSIFICATION		15	16	17	18	19
20	21	22	23	24	25	26
27	28	29	30	31	32	33
34	35	36	37	38	39	40
41	42	43	44	45	46	47
48	49	50	51	52	53	54
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90	91	92	93	94	95	96
97	98	99	100	101	102	103
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket						

TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drawn	Flgs. Drawn	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent, currently to _____ (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____.					ISSUE FEE	
		(Assistant Examiner) _____ (Date) _____ (Primary Examiner) _____ (Date) _____			Amount Due _____ Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					ISSUE BATCH NUMBER	
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